

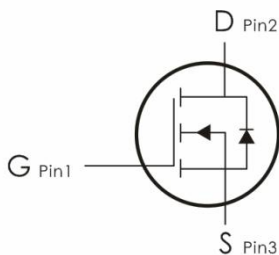
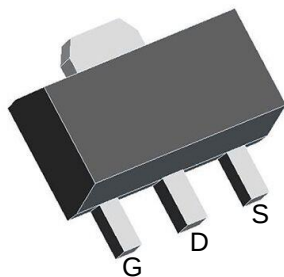
Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=40V, I_D=16A, R_{DS(on)} < 16m\Omega @ V_{GS}=10V$ (Typ: $12m\Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
WD016NG	D016N	SOT-89	1000 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	16	A
I_{DM}	Pulsed Drain Current ¹	40	
E_{AS}	Single pulse avalanche energy ²	36	mJ
P_D	Power Dissipation	2.4	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55--+150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ³	52	$^\circ C/W$

Electrical Characteristics: (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	40	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =40V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	1.3	1.8	V
R _{DS(ON)}	Drain-Source On Resistance ³	V _{GS} =10V, I _D =10A	---	12	16	m Ω
		V _{GS} =4.5V, I _D =5A	---	14	18	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	1113	---	pF
C _{OSS}	Output Capacitance		---	109	--	
C _{rss}	Reverse Transfer Capacitance		---	88	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =20V, I _D =10A, R _{ENG} =3 Ω, V _{GS} =10V	---	7	---	ns
t _r	Rise Time		---	11.5	---	ns
t _{d(off)}	Turn-Off Delay Time		---	27	---	ns
t _f	Fall Time		---	5	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =20V, I _D =10A	---	27	---	nC
Q _{gs}	Gate-Source Charge		---	6.3	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	5	---	nC
Drain-Source Diode Characteristics						
I _S	Continuous Drain Curren	V _D =V _G =0V	---	---	16	A
I _{SM}	Pulsed Drain Current		---	---	40	A
T _{rr}	Reverse Recovery Time	I _F =15A, T _J =25 °C	---	10	---	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/us	---	6	---	nC
V _{SD}	Diode Forward Voltage ³	V _{GS} =0V, I _{SD} =10A	---	---	1.2	V

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition : $T_J=25^{\circ}\text{C}$, $V_{DD}=20\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Test Circuit

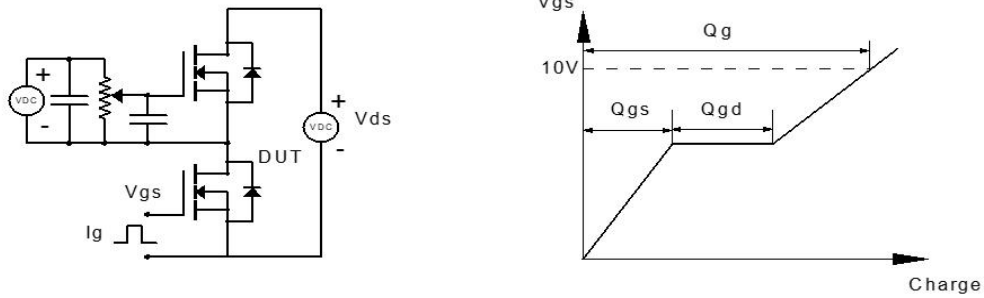


Figure 1: Gate Charge Test Circuit & Waveform

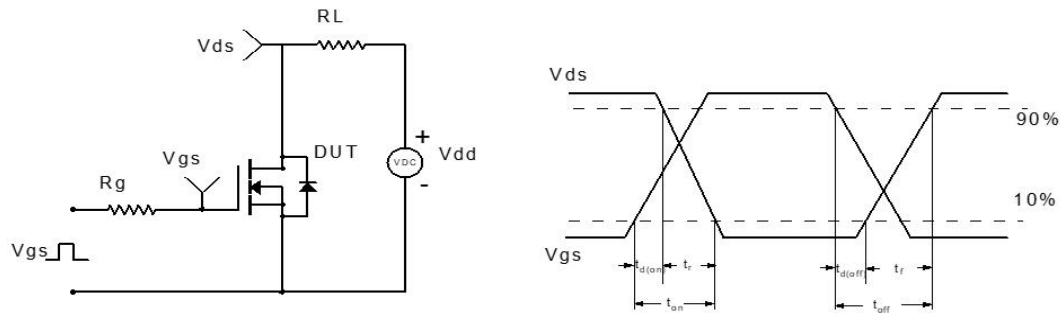


Figure 2: Resistive Switching Test Circuit & Waveform

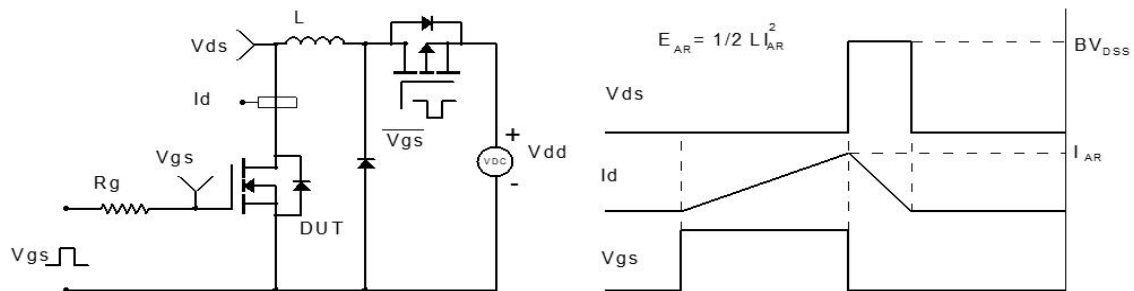


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

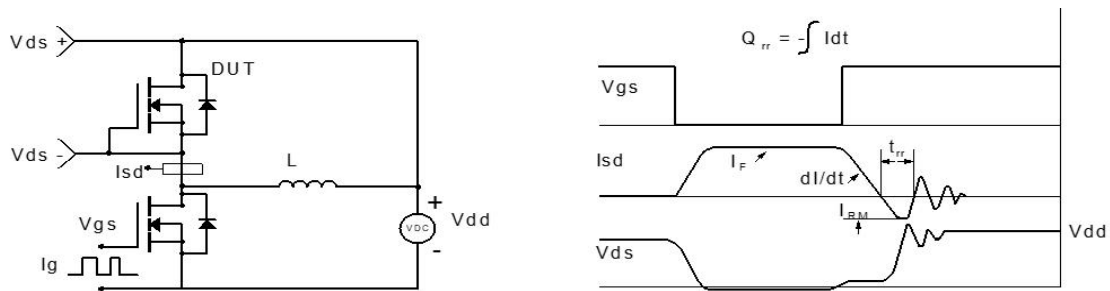
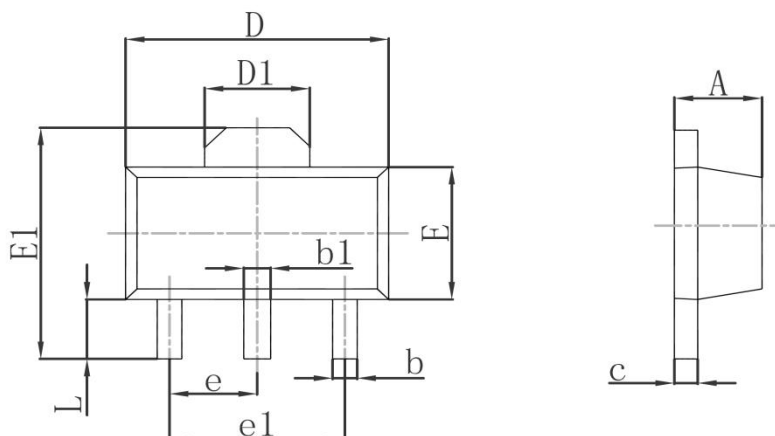


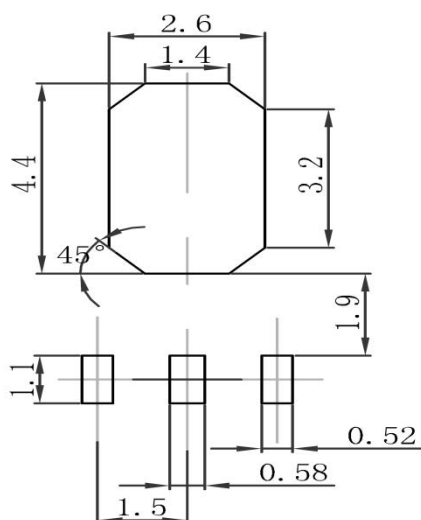
Figure 4: Diode Recovery Test Circuit & Waveform

SOT-89 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550REF		0.061REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500TYP		0.060TYP	
e1	3.000TYP		0.118TYP	
L	0.900	1.200	0.035	0.047

SOT-89 Suggested Pad Layout



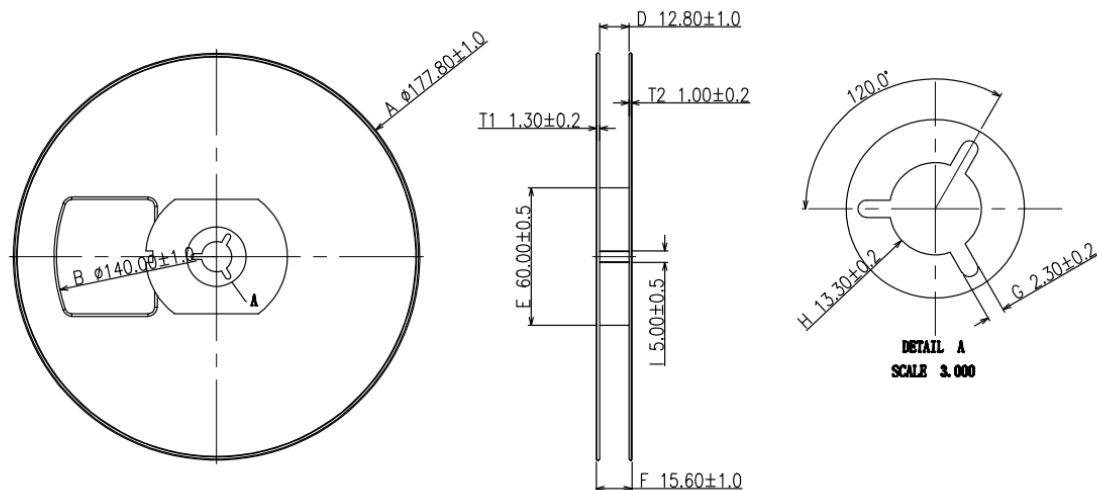
Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purposes only

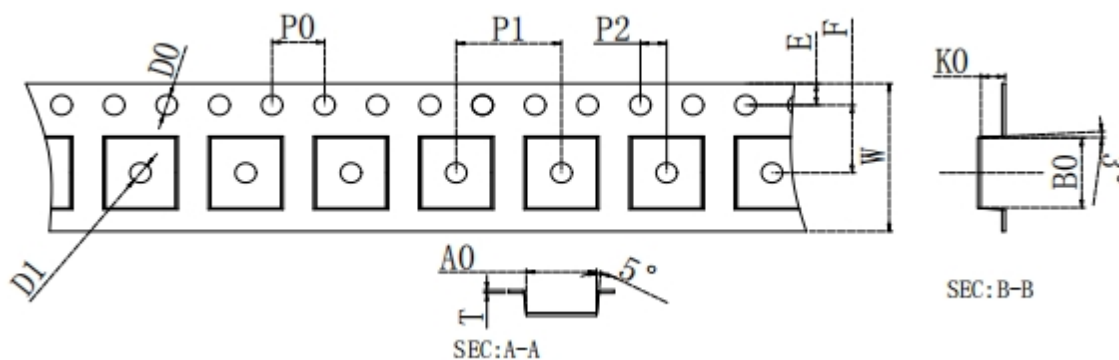
UNIT: mm

Tape & Reel Information

Dimensions in mm



W	12.00 ± 0.10	T	0.22 ± 0.02	D1	1.60 ± 0.10	MM
E	1.75 ± 0.10	F	5.50 ± 0.10	D0	1.60 ± 0.10	PC+PS
PO	4.00 ± 0.10	P1	8.00 ± 0.10	P2	2.00 ± 0.10	SOT89
A0	4.90 ± 0.10	B0	4.50 ± 0.10	K0	1.75 ± 0.10	



Pulling direction →

Marking Information:

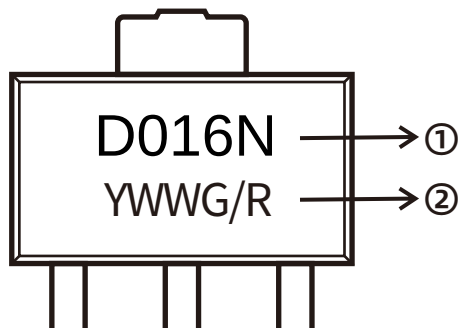
①. Part NO.

②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2024-10-20	Release of final version

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